	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	770	(vlsi or chip or asci) and network and packet and port and (ingress or input) and (egress or output) and (switch same fabric) and (flow) and (processor or controller) and (table or database) and (memory or buffer or ram)	US-PGPUB; USPAT	2005/02/17 21:08	
2	BRS	L2	411	1 and (parallel same process$5)	US-PGPUB; USPAT	2005/02/17 21:09	
3	BRS	L3	329	2 and (packet same rout$5)	US-PGPUB; USPAT	2005/02/17 21:10	
4	BRS	L4	283	3 and (network same control$5)	US-PGPUB; USPAT	2005/02/17 21:11	
5	BRS	L5	226	4 and interface and mode and select$5	US-PGPUB; USPAT	2005/02/17 21:12	

	Type	Hits	Search Text	DBs
1	BRS	660	chip and (processor or controller) and packet and (switch or route) and memory and traffic and port and (qos or (quality same of same service))	US-PGPUB; USPAT
2	BRS	312	S1 and (input or ingress) and (output or egress) and fabric	US-PGPUB; USPAT
3	BRS	218	S2 and schedule$2	US-PGPUB; USPAT
4	BRS	218	S3 and data	US-PGPUB; USPAT
5	BRS	180	S4 and frame$2	US-PGPUB; USPAT
6	BRS	176	S5 and (table or database)	US-PGPUB; USPAT
7	BRS	133	S6 and (programs or software) and hardware	US-PGPUB; USPAT